

PATENT
Docket No. 15436.367.1

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Dybsetter et al.

Serial No.: 10/814,449

Art Unit
2112

Filed: Mar. 31, 2004

Confirmation No.: 5452

For: *TWO-WIRE INTERFACE HAVING EMBEDDED PER FRAME RELIABILITY INFORMATION*

Examiner: Shelly Case

Patent No.: 7,287,208 B2

Issue Date: Oct. 23, 2007

REQUEST FOR A CERTIFICATE OF CORRECTION
UNDER 37 C.F.R. § 1.323

*Certificate
SEP 09 2010
of Correction*

Attention: Certificate of Corrections Branch
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

09/08/2010 MBLANCO 08000032 7287208
81 FC:1811 100.00 OP

Dear Commissioner:

In accordance with the provisions of 37 C.F.R. § 1.323, which implements 35 U.S.C. § 255, the Director is respectfully requested to issue a Certificate of Correction to correct mistakes in the above-identified patent. The enclosed Patent Office Form PTO/SB/44 reflects the desired corrections.

Payment in the amount of \$100, as set forth in 37 C.F.R. § 1.20(a) is submitted herewith to cover the costs for issuance of the requested Certificate of Correction. The Commissioner is hereby authorized to charge payment of any of the following fees that may be applicable to this communication, or credit any overpayment, to Deposit Account No. 23-3178: (1) any filing fees

required under 37 CFR § 1.16; (2) any patent application and reexamination processing fees under 37 CFR § 1.17; and/or (3) any post issuance fees under 37 CFR § 1.20. In addition, if any additional extension of time is required, which has not otherwise been requested, please consider this a petition therefore and charge any additional fees that may be required to Deposit Account No. 23-3178.

Dated this 25 day of June, 2010.

Respectfully submitted,



KULANIAKEA FISHER
Attorney for Applicant
Registration No.: 50987
Customer No.: 22913

WORKMAN NYDEGGER
1000 Eagle Gate Tower
60 East South Temple
Salt Lake City, UT 84111
Phone: 801-533-9800
Fax: 801-328-1707

CERTIFICATE OF DEPOSIT UNDER 37 C.F.R. § 1.8

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail, postage prepaid, in an envelope addressed to: Attention: Certificate of Corrections Branch, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 319 day of September, 2010.

Respectfully submitted,



Transmitted: REQUEST FOR A CERTIFICATE OF CORRECTION UNDER 37 C.F.R. § 1.323
PTO Form PTO/SB/44 and check No. 169740 in the amount of \$100

KF:sv
Proofreading - Request for COC



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Page 1 of 6

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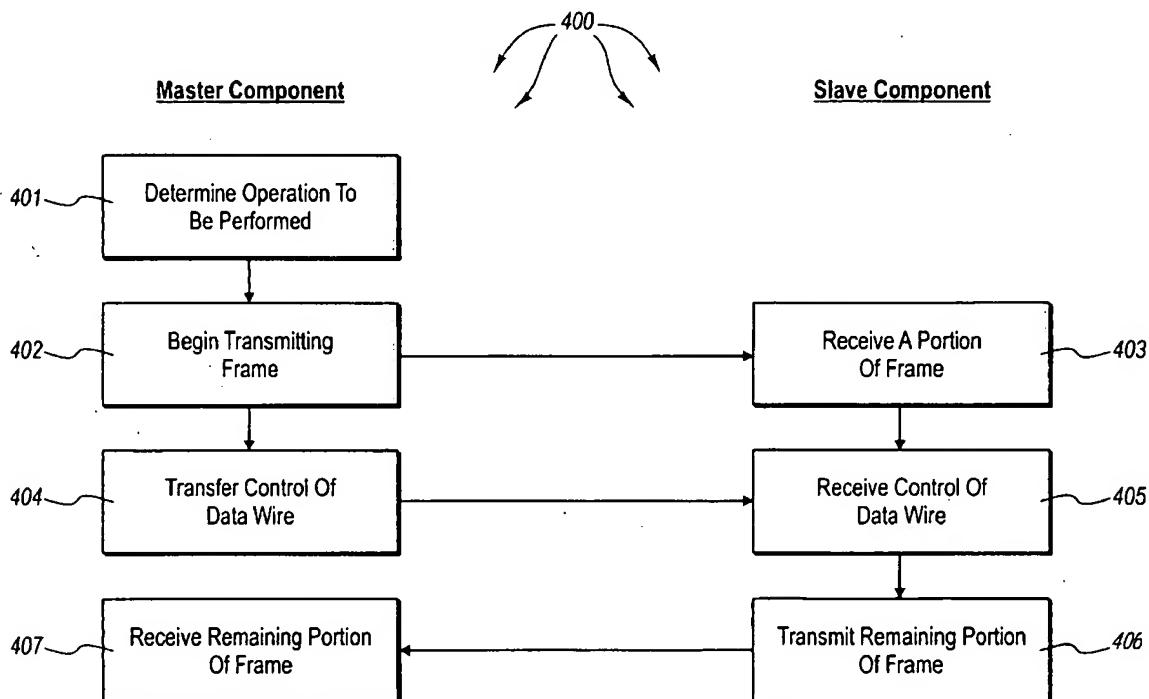
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Replace Figure with the figure depicted herein below, wherein the second instance of "405" referring to "Transmit Remaining Portion of Frame" has been changed to --406--



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Line 6, change "component is" to --component that is--

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Line 24, change "and" to --an--

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Line 45, change "bit rates transfers" to --bit rate transfers--

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Line 26, change "signal" to --line--

Line 51, change "16bit" to --16-bit--

Line 61, change "control" to --controller--

Column 14

Line 4, change "being" to --is--

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Claim 11, line 52, change "indication" to --indicating--

Claim 11, line 55, change "component;" to --component; and--

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Claim 19, line 3, change "that indicative" to --indicative--

Claim 25, line 25, change "slave component" to --master component--

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Column 18**Claim 31, line 12, change “that indicative” to --indicative--****Claim 31, line 22, change “that indicative” to --indicative--****Claim 39, line 45, change “38” to --37--****Claim 40, line 48, change “38” to --37--****Claim 41, line 51, change “38” to --37--****Claim 42, line 54, change “38” to --37--****Claim 43, line 57, change “38” to --37--****Claim 44, line 60, change “38” to --37--****Claim 45, line 63, change “38” to --37--**

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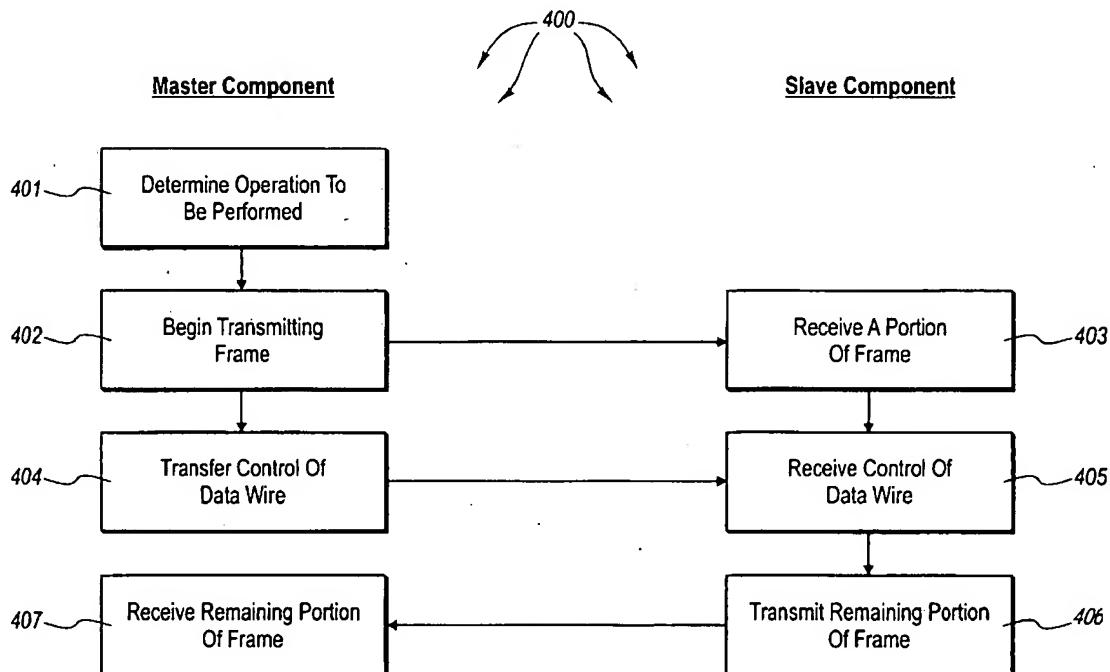
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It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Drawings

Sheet 5, replace Figure 4 with the figure depicted herein below, wherein the second instance of "405" referring to "Transmit Remaining Portion of Frame" has been changed to --406--



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Drawings

Sheet 7, replace Figure 7 with the figure depicted herein below, wherein under the "Bits" column: "28:26" has been changed to --28:24--, "25:19" has been changed to --23:19--, "16:1" has been changed to --16:01--, and "0" has been changed to --00--

	PRE	ST	OP	PRT	DEV	TA	ADDRESS/DATA	IDLE
	6	3	33	32	2	2	1	11 1
	4	3	21	09	8	4	3	9 87 6
<hr/>								
Address	1	...	1	00	00	PPPPP	EEEEEE	10 AAAAAAAAAAAAAA Z
Write	1	...	1	00	01	PPPPP	EEEEEE	10 DDDDDDDDDDDDDDDDD Z
Read	1	...	1	00	11	PPPPP	EEEEEE	Z0 DDDDDDDDDDDDDDDDD Z
Read inc.	1	...	1	00	10	PPPPP	EEEEEE	Z0 DDDDDDDDDDDDDDDDD Z
<hr/>								
Field	Bits							
PRE	64:33	(32)	--	preamble				
ST	32:31	(2)	--	start of frame				
OP	30:29	(2)	--	operation code				
	(ADDR=00,WR=01,RD=11,RDINC=10)							
PRTAD	28:24	(5)	--	port address				
DEVAD	23:19	(5)	--	device address				
TA	18:17	(2)	--	bus turnaround phase & transfer acknowledge				
ADDR/DATA	16:01	(16)	--	address or data				
IDLE	00	(1)	--	end of transmission				
<hr/>								
	65	--	transaction bit length					

Fig. 7
(Prior Art)

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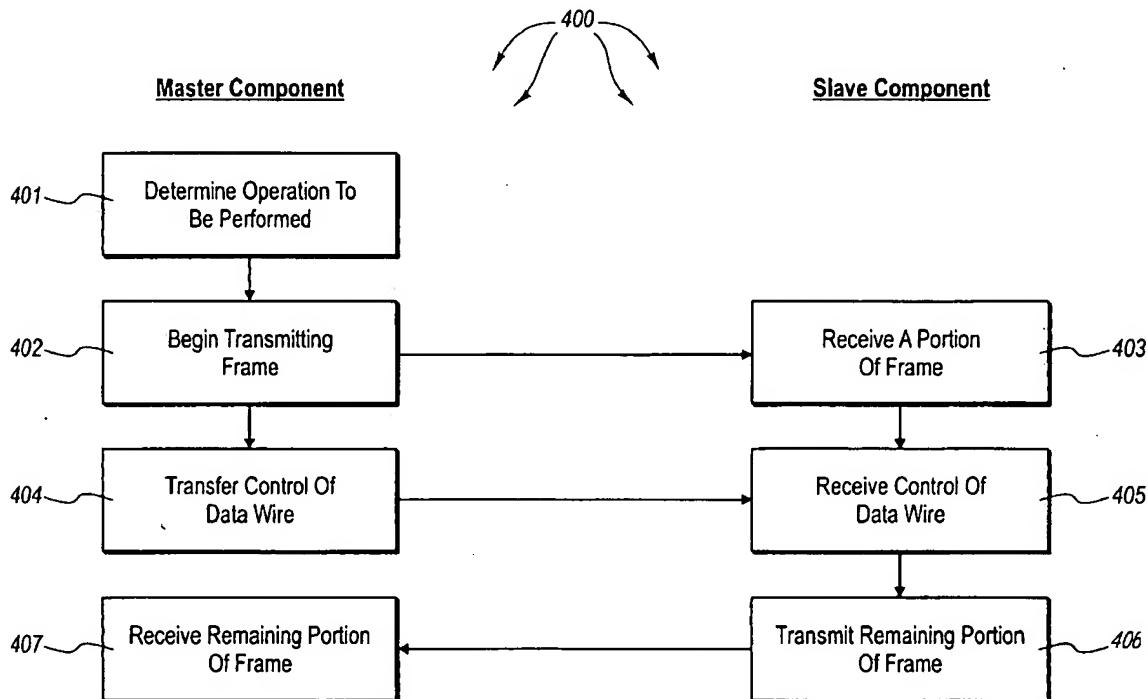
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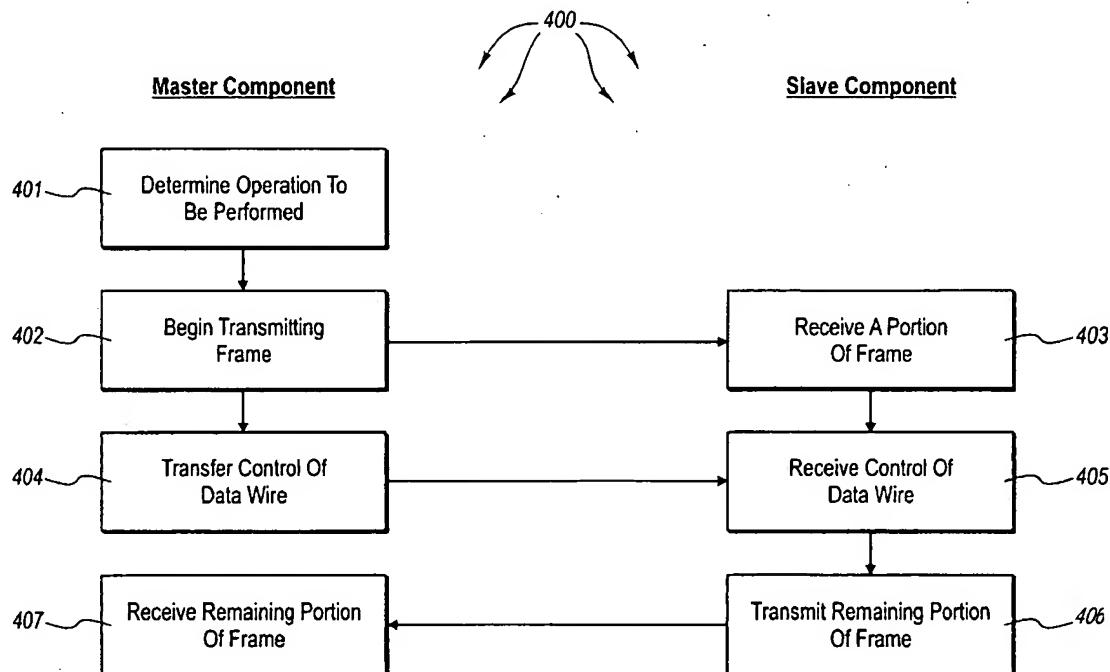
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UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

Page 6 of 6

PATENT NO. : 7,287,208 B2

APPLICATION NO. : 10/814,449

ISSUE DATE : Oct. 23, 2007

INVENTOR(S) : Dybsetter et al.

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Drawings

Sheet 7, replace Figure 7 with the figure depicted herein below, wherein under the "Bits" column: "28:26" has been changed to --28:24--, "25:19" has been changed to --23:19--, "16:1" has been changed to --16:01--, and "0" has been changed to --00--

	PRE	ST	OP	PRT	DEV	TA	ADDRESS/DATA	IDLE
	6	3 33	32 2	2 2	1 11	1		0 0
	4	3 21	09 8	4 3	9 87	6		1 0
<hr/>								
Address	1...1	00 00	PPPPP	EEEEEE	10	AAAAAAAAAAAAAA	Z	
Write	1...1	00 01	PPPPP	EEEEEE	10	DDDDDDDDDDDDDDDD	Z	
Read	1...1	00 11	PPPPP	EEEEEE	20	DDDDDDDDDDDDDDDD	Z	
Read inc.	1...1	00 10	PPPPP	EEEEEE	20	DDDDDDDDDDDDDDDD	Z	
<hr/>								
Field	Bits							
PRE	64:33 (32) -- preamble							
ST	32:31 (2) -- start of frame							
OP	30:29 (2) -- operation code (ADDR=00,WR=01,RD=11,RDINC=10)							
PRTAD	28:24 (5) -- port address							
DEVAD	23:19 (5) -- device address							
TA	18:17 (2) -- bus turnaround phase & transfer acknowledge							
ADDR/DATA	16:01 (16) -- address or data							
IDLE	00 (1) -- end of transmission							
<hr/>								
	65 -- transaction bit length							

Fig. 7
(Prior Art)

MAILING ADDRESS OF SENDER (Please do not use customer number)

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